

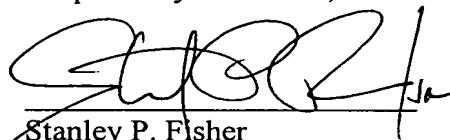
28. (Added) A signal processing circuit according to Claim 22, further comprising a third input-output circuit that receives or transmits a serial signal that is either an encoded video signal or an encoded voice signal, produces an interrupt request to be issued to said microprocessor, and transfers data of the serial signal over said system bus under control of said microprocessor.

REMARKS

Applicant has amended claim 3 and added claim 11, amended claim 4 and added claims 12, 13 and 14, amended claim 5 and added claims 15, 16 and 17, amended claim 6 and added claims 18, 19 and 20, amended claim 7 and added claim 21, amended claim 8 and added claim 22, amended claim 9 and added claims 23, 24, and 25, and amended claim 10 and added claims 26, 27 and 28. Applicants have amended the claims in order to remove the multiple dependencies contained therein in accordance with standard U.S. practice, thereby reducing the basic filing fee. No new matter has been added to the application as a result of this amendment.

In view of the above amendments and Applicants' comments stated herein, Applicants respectfully request an early and favorable action on the merits.

Respectfully submitted,



Stanley P. Fisher

Registration Number 24,344

JUAN CARLOS A. MARQUEZ
Registration No. 34,072

REED SMITH HAZEL & THOMAS LLP

3110 Fairview Park Drive

Suite 1400

Falls Church, Virginia 22042

(703) 641-4200

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